

## IN THE SPECIFICATION

Please amend the paragraph beginning on page 5, line 19 as follows:

Figure 4 is a schematic depiction of instructions 125 in accordance with one embodiment of the present invention. The instructions 125 include ~~five~~ six separate instructions from I0 to I5, all of which are shown as assembly language instructions that can be executed by the processor 20 of system 10 shown in Figure 1. First instruction I0 indicates a move instruction that moves contents from register r02 to register r1. Likewise, instruction I1 indicates moving content of register r02 into another location. In this manner, ~~five~~ six exemplary instructions as code are shown for scheduling in accordance with one embodiment of the present invention.

Please amend the paragraph beginning on page 6, line 17 as follows:

Figure 6 is a hypothetical pseudo code 130 showing a heuristic rule for scheduling instructions 125 shown in Figure 4 in accordance with one embodiment of the present invention. If the GAPs of all instructions are zeros, there is no need to schedule the instructions, as in-order execution is just the most efficient way. If any non-zero GAP exists, however, a simple heuristic rule in Figure 6 [[7]] with linear complexity of order O(n) may eliminate most of GAPs in many Java applications.